Attorney Docket No.: 49581/P042US/10315832

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Response to Restriction Requirement – 3 pages

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TRANSM	ITTAL		Filing Date		March 24, 2004
FOR			First Named Inventor		Robert A. Greene
(to be used for all correspond	(to be used for all correspondence after initial filing)		Art Unit		2814
(10 00 000 00 00			Examiner Name		L. Pham
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Firm Name FULBRIGHT	& JAWORSI	KI L.L.P.			
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Firm Name	FULBRIGHT & JAWORSKI L.L.P.		
Signature	Jash Shot		
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Date	September 29, 2005	Reg. No.	44,034





Docket No.: 49581/P042US/10315832

(PATENT)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:

Robert A. Greene

Application No.: 10/807,789

Filed: March 24, 2004

For: SYSTEM AND METHOD FOR COUPLING

INTERNAL CIRCUITRY OF AN INTEGRATED CIRCUIT TO THE

INTEGRATED CIRCUIT'S PACKAGE PINS

Confirmation No.: 4254

Art Unit: 2814

Examiner: L. Pham

RESPONSE TO RESTRICTION REQUIREMENT

MS Amendment Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Dear Sir:

In response to the restriction requirement set forth in the Office Action mailed August 29, 2005, applicant hereby provisionally elects claims 1-17 and 32-44 for continued examination, WITH TRAVERSE. Applicant further provisionally elects the specie of FIGURE 1, WITH TRAVERSE.

<u>REMARKS</u>

I. Claim Restriction

The Examiner has required restriction between:

Group 1: Claims 18-31, drawn to a semiconductor process, classified in class 438, subclass 106; and